Docket No.

252121US2S

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF:

Keiichi KUSHIDA, et al.

SERIAL NO:

New Application

GAU:

FILED:

Herewith

EXAMINER:

FOR:

SEMICONDUCTOR DEVICE WITH MEMORY AND METHOD FOR MEMORY TEST

INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR 1.97

COMMISSIONER FOR PATENTS ALEXANDRIA, VIRGINIA 22313

SIR:

Applicant(s) wish to disclose the following information.

REFERENCES

- The applicant(s) wish to make of record the references listed on the attached form PTO-1449. Copies of the listed references are attached, where required, as are either statements of relevancy or any readily available English translations of pertinent portions of any non-English language references.
- A check or credit card payment form is attached in the amount required under 37 CFR §1.17(p).

RELATED CASES

- Attached is a list of applicant's pending application(s) or issued patent(s) which may be related to the present application. A copy of the patent(s), together with a copy of the claims and drawings of the pending application(s) is attached along with PTO 1449.
- A check or credit card payment form is attached in the amount required under 37 CFR §1.17(p).

CERTIFICATION

- ☐ Each item of information contained in this information disclosure statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this statement.
- □ No item of information contained in this information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application or, to the knowledge of the undersigned, having made reasonable inquiry, was known to any individual designated in 37 CFR §1.56(c) more than three months prior to the filing of this statement.

DEPOSIT ACCOUNT

Please charge any additional fees for the papers being filed herewith and for which no check or credit card payment is enclosed herewith, or credit any overpayment to deposit account number <u>15-0030</u>. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

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LIST OF RELATED CASES

Docket Number	Serial or Patent Number	Filing or <u>Issue Date</u>	Inventor/ <u>Applicant</u>
246496US2S	10/734,303	12/15/03	KUSHIDA
246825US2S	10/739,123	12/19/03	HIRABAYASHI

Form PTO 1449 U.S. DEPARTMENT OF COMMERC (Modified) PATENT AND TRADEMARK OFFIC			ATTY DOCKET NO.		SERIAL NO. New Application						
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LIST OF REFERENCES CITED BY APPLICANT			Keiichi KUSHIDA, et al.								
		FILING DATE		GROUP							
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U.S. PATENT DOCUMENTS											
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS					
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		OTHER RE	FERENCES	(Including Author, Title, Date, Pertinen	t Pages, e	tc.)					
	AW	Kiyohiro FURUTANI, et al., "A BUILT-IN HAMMING CODE ECC CIRCUIT FOR DRAM'S", IEEE Journal of Solid-State Circuits, Vol. 24, No. 1, February 1989, pgs. 50-56									
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	AZ			Additional References sheet(s) attached							
Examiner					Date Considered						
*Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.											